

Fig. 1

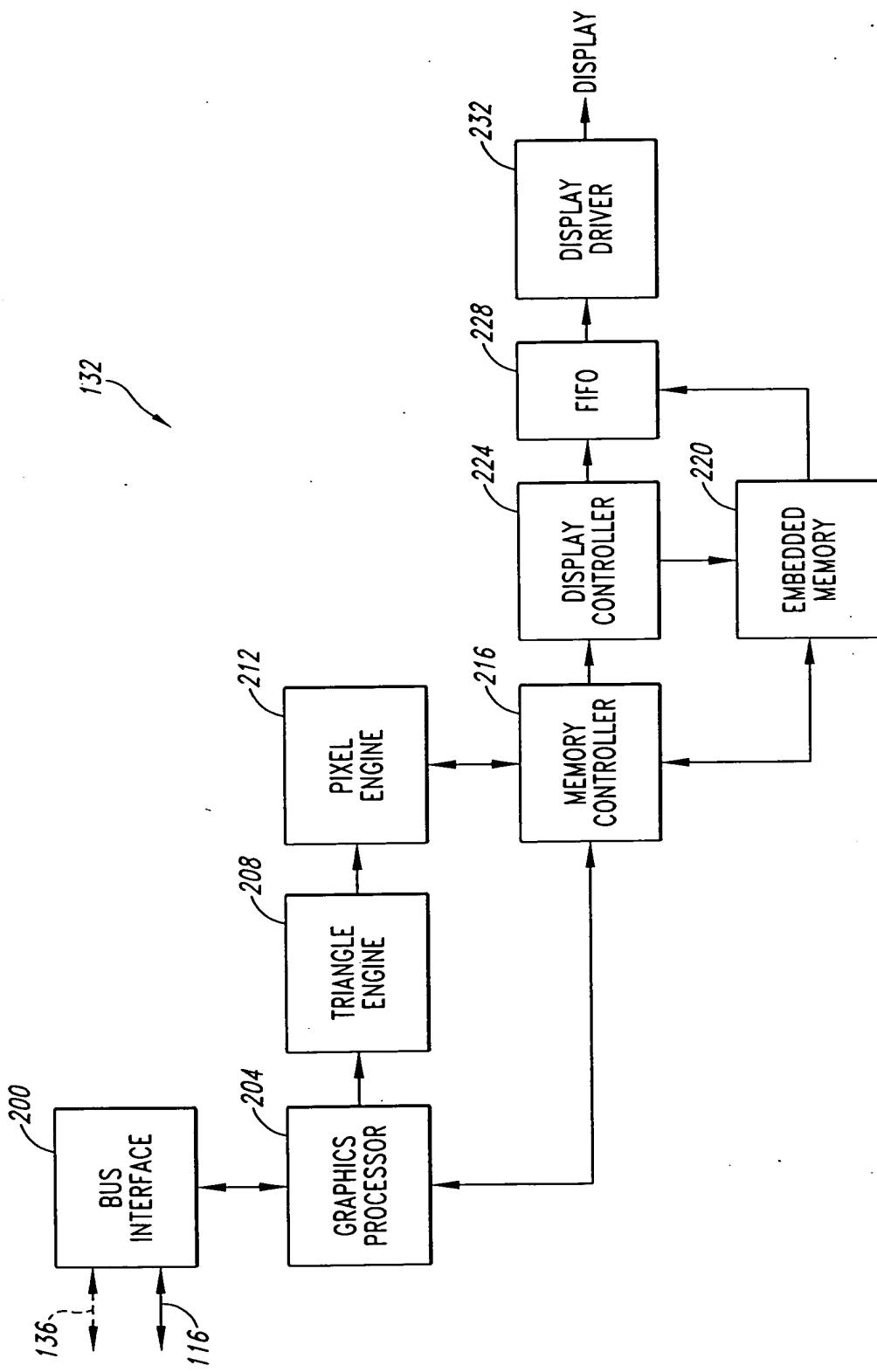


Fig. 2

312	MEMORY VALID = 1111
316	BANK0 = 00
320	BANK1 = 01
324	BANK2 = 10
330	BANK3 = 11

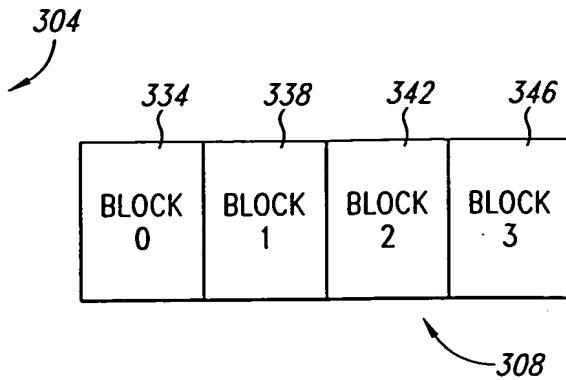


Fig. 3A

312	MEMORY VALID = 1011
316	BANK0 = 00
320	BANK1 = 10
324	BANK2 = 11
330	BANK3 = XX

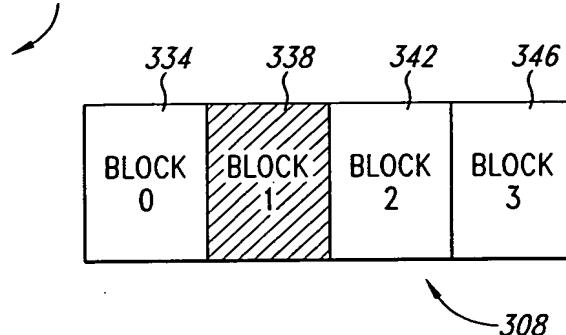


Fig. 3B

312	MEMORY VALID = 1100
316	BANK0 = 00
320	BANK1 = 01
324	BANK2 = XX
330	BANK3 = XX

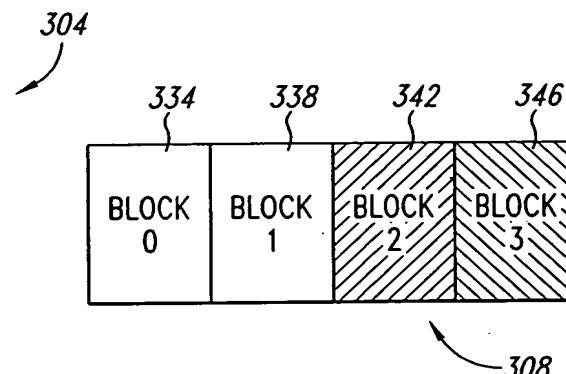


Fig. 3C

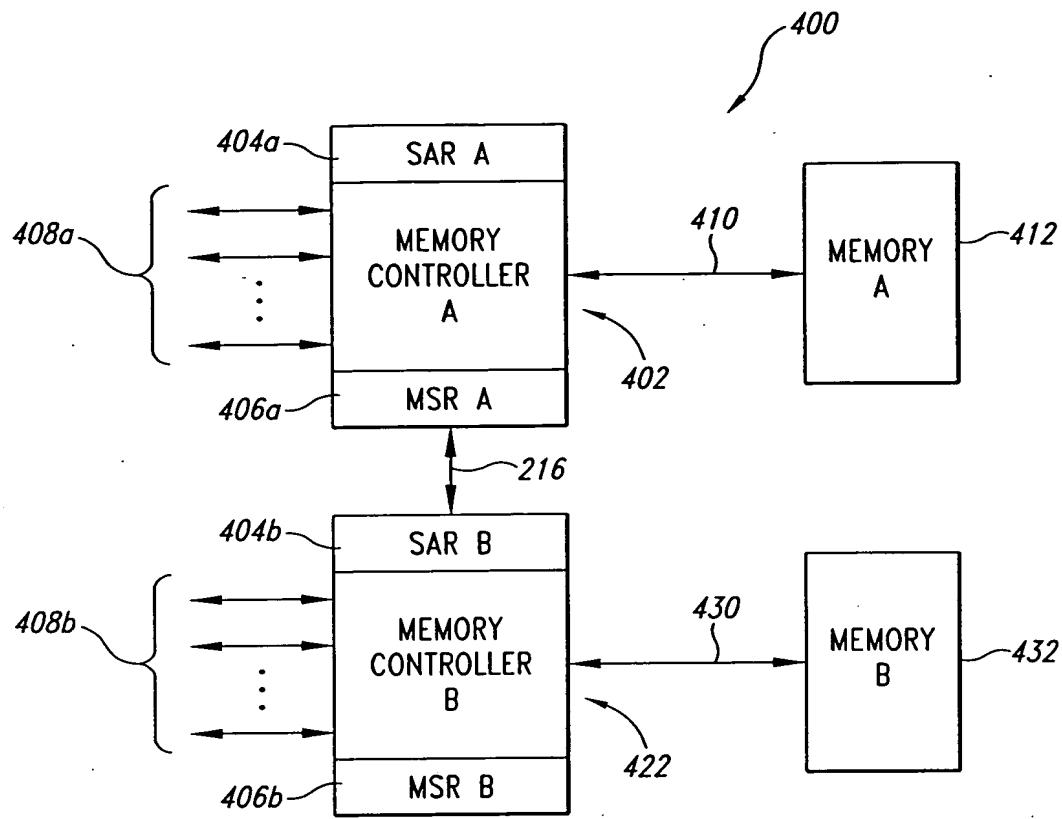


Fig. 4

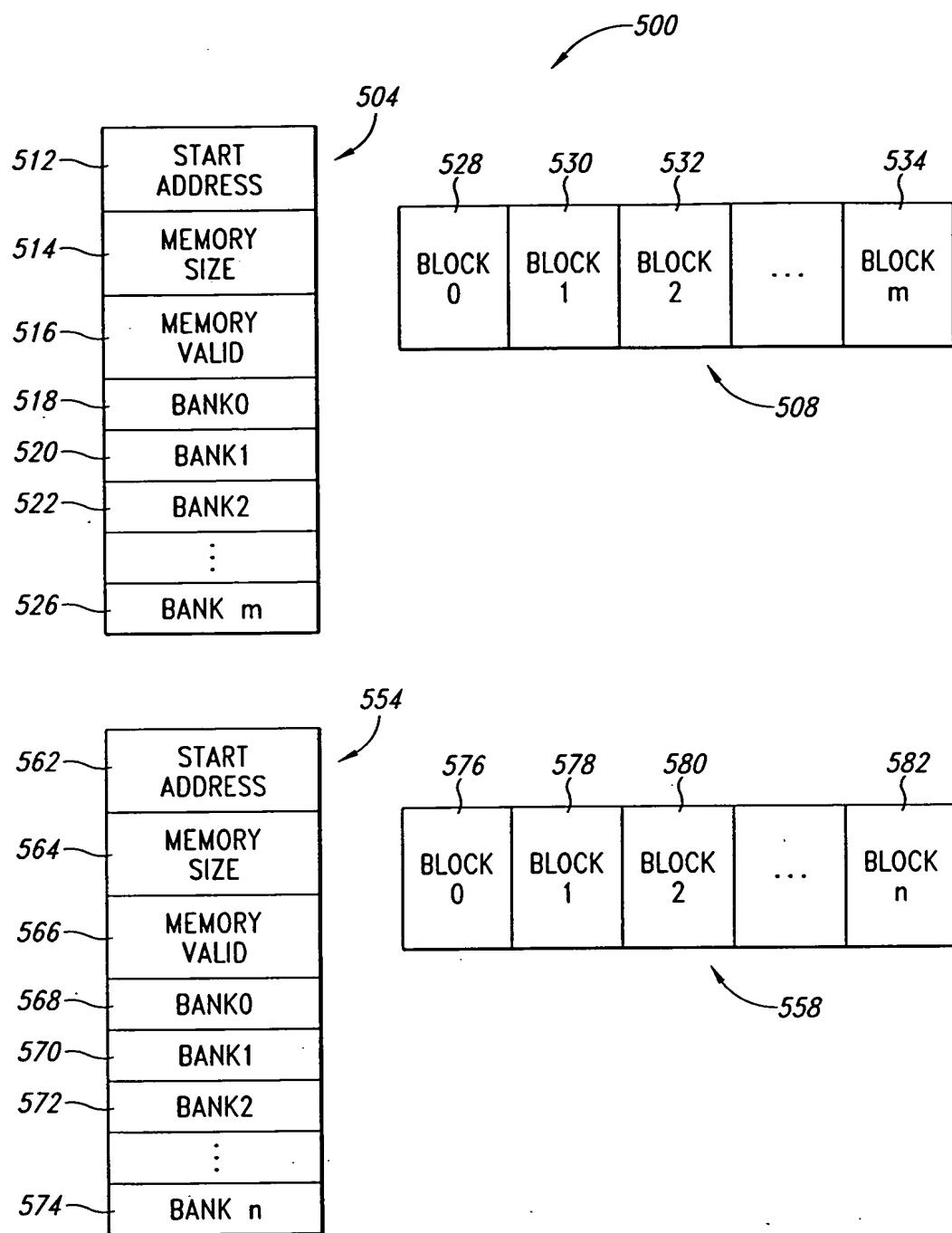


Fig. 5